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				1T	First Named Inventor:	Sara Ruhina Biyabani		
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Sheet	1		of		2	Attorney Docket No.	04860.F	2438
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Examiner		Date	
Signature	/Antonio Caschera/	Considered	04/26/2007

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Substitute for Form 1449/PTO Complete if Known 09/589,621 **Application Number** INFORMATION DISCLOSURE Filing Date 06/07/2000 STATEMENT BY APPLICANT First Named Inventor: Sara Ruhina Biyabani (use as many sheets as necessary) Art Unit 2628 Examiner Name Caschera, Antonio A. 2 Sheet 2 of Attorney Docket No. 04860.P2438 NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the T² Examiner Cite No¹ item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Initials* number(s), publisher, city and/or country where published JUPITERMEDIA CORPORATION. "Integrated Video: Breaking the last PC Taboo," 802.11 Planet Conference & Expo World Tour, downloaded from the Internet at: /AC/ www.cpuplanet.com/features/article.php/1566521, © 2003 Jupitermedia Corporation (2 pages). KOCSIS, DAVID. "Frame-buffer wars: new directions in PC graphics," EDN Access for /AC/ Design, by Design, May 9, 1996 (10 pages). SILICON GRAPHICS, INC. "O2 Unified Memory Architecture," Silicon Graphics, Inc., 1997 /AC/ (pp. 1-6). WILLIAMS, IAN. "An Explanation of the Advantages Afforded by QED RM5200 and MIPS /AC/ R12000 CPUs in O2," SGI White Paper, 1999 (pp. 1-41).

	Examiner	Matania Casabaral	Date	04/26/2007
1	Signature	/Antonio Caschera/	Considered	04/20/2007

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